



Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Package	I _D T _C = +25℃
650V	1.3Ω @ V _{GS} = 10V	TO-220AB	9.0 A

Description

This new generation complementary dual MOSFET features low onresistance and fast switching, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

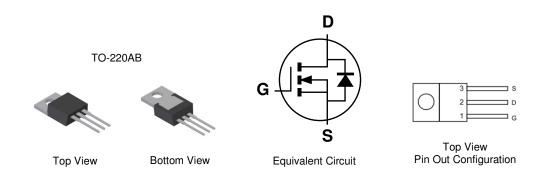
- Low Input Capacitance
- High BVDss rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

N-CHANNEL ENHANCEMENT MODE MOSFET

• Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: TO-220AB
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram Below
- Weight: TO-220AB 1.85 grams (Approximate)



Ordering Information (Note 4)

-		
Part Number	Case	Packaging
DMG9N65CT	TO-220AB	50 pieces/tube

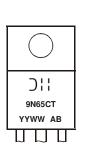
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



TO-220AB

9N65CT = Product Type Marking Code AB = Foundry and Assembly Code YYWW = Date Code Marking YY = Last two digits of year (ex: 11 = 2011) WW = Week (01 - 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	650	V
Gate-Source Voltage			V _{GSS}	±30	V
Continuous Drain Current (Note 5) V_{GS} = 10V	Steady State	T _C = +25 ℃ T _C = +70 ℃	ID	9.0 7.0	A
Pulsed Drain Current (Note 6) 10us pulse, pulse duty cycle<=1%			I _{DM}	30	A
Avalanche Current (Note 7) V_{DD} = 100V, V_{GS} = 10V, L = 60mH			I _{AR}	2.7	A
Repetitive Avalanche Energy (Note 7) V_{DD} = 100V, V_{GS} = 10V, L = 60mH			E _{AR}	260	mJ

Thermal Characteristics

Characteristic	Symbol	Мах	Unit
Power Dissipation (Note 5) $T_C = +25 \degree C$ $T_C = +70 \degree C$	PD	165 100	w
Thermal Resistance, Junction to Case (Note 5)	R _{θJC}	0.7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

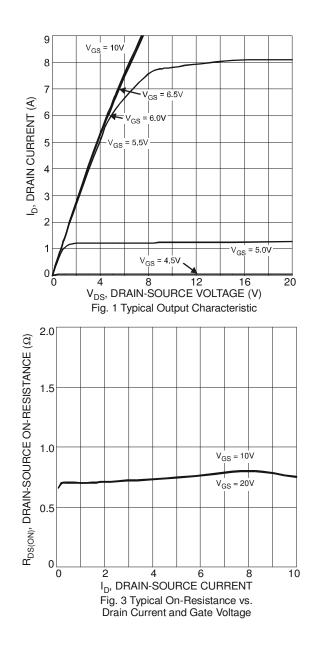
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			1	r		1	
Drain-Source Breakdown Voltage	BV _{DSS}	650		—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current TJ = +25℃	IDSS			1.0	μA	$V_{DS} = 650V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	3	—	5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	—	0.7	1.3	Ω	$V_{GS} = 10V, I_D = 4.5A$	
Forward Transfer Admittance	Y _{fs}	—	8.5	—	S	$V_{DS} = 40V, I_D = 4.5A$	
Diode Forward Voltage	V _{SD}	_	0.7	1.0	V	$V_{GS} = 0V, I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	2,310		pF	$\label{eq:VDS} \begin{array}{l} V_{DS} = 25V, V_{GS} = 0V, \\ f = 1.0MHz \end{array}$	
Output Capacitance	Coss	_	122	—			
Reverse Transfer Capacitance	C _{rss}	_	2.2	—			
Gate Resistance	Rg	_	2.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge V _{GS} = 10V	Qg	—	39	—		$\label{eq:VGS} \begin{array}{l} V_{GS} = 10V, \ V_{DS} = 520V, \\ I_{D} = 8A \end{array}$	
Gate-Source Charge	Q _{gs}	_	8.5	_	nC		
Gate-Drain Charge	Q _{gd}	—	11.9	—			
Turn-On Delay Time	t _{D(on)}	_	39	_	ns	V _{GS} = 10V, V _{DS} = 325V,	
Turn-On Rise Time	tr	—	29	—	ns		
Turn-Off Delay Time	t _{D(off)}	_	122	—	ns	$R_G=25\Omega,\ I_D=8A$	
Turn-Off Fall Time	t _f	_	28	_	ns]	
Body Diode Reverse Recovery Time	t _{rr}	_	570	—	ns	dl/dt = 100A/µs, V _{DS} = 100V,	
Body Diode Reverse Recovery Charge	Q _{rr}		4.17		μC	I _F = 8A	

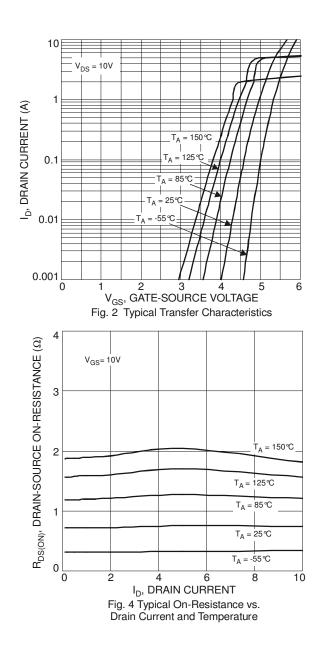
Notes:

Device mounted on an infinite heatsink.
Repetitive rating, pulse width limited by junction temperature.

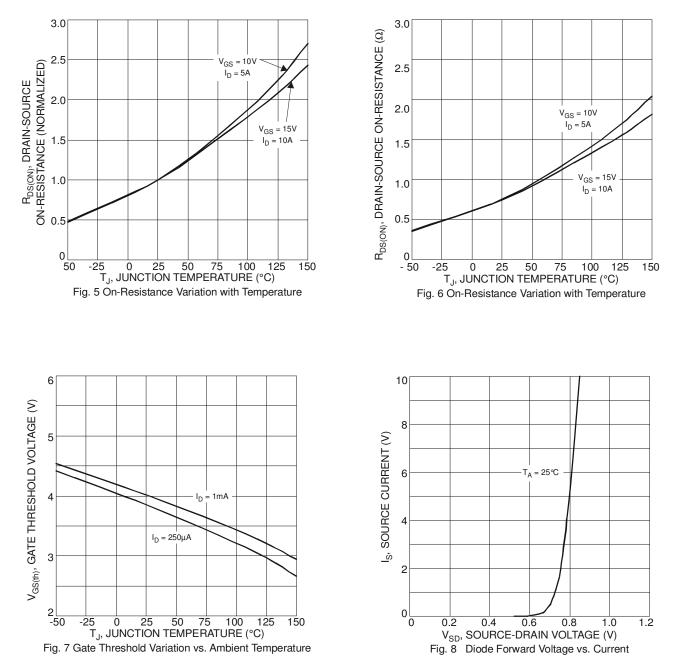
7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = +25$ °C. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to production testing.



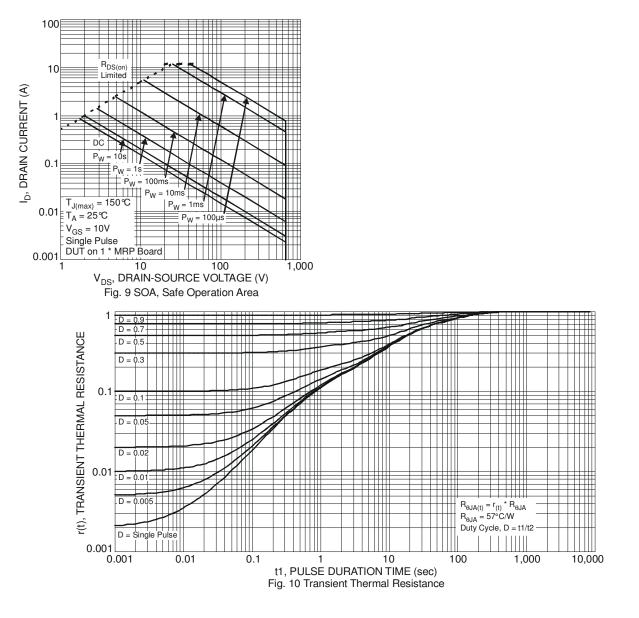








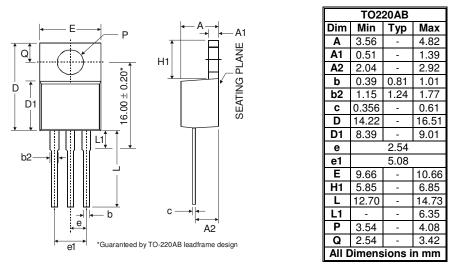






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



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